

Implementing SPHINCS with restricted memory

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Radboud University

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 - ▶ Relevant crypto context
 - ▶ SPHINCS
 - ▶ Implementation details

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- ▶ Not this talk:
 - ▶ Background on public key crypto / hashes in general
 - ▶ Other post-quantum crypto
 - ▶ Quantum computing / crypto

Cryptographic context

- ▶ SPHINCS¹: Stateless, practical, **hash-based**, incredibly nice cryptographic signatures
- ▶ Hashes do not fall to Shor (but halved by Grover)
- ▶ Hash-based schemes: conservative choice post-quantum
 - ▶ Fundamental building block

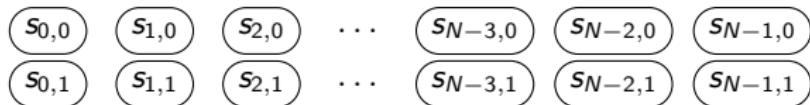
¹Daniel J. Bernstein, Diana Hopwood, Andreas Hülsing, Tanja Lange, Ruben Niederhagen, Louiza Papachristodoulou, Peter Schwabe and Zooko Wilcox O'Hearn, 2015

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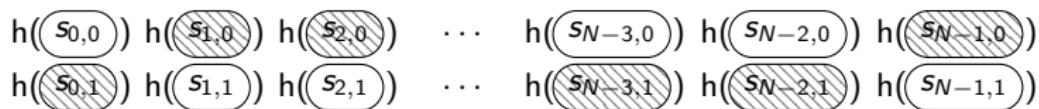
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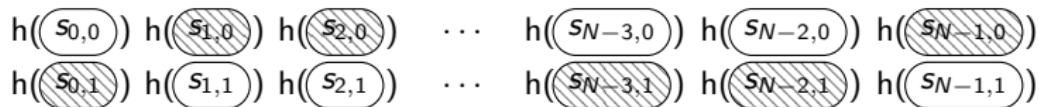
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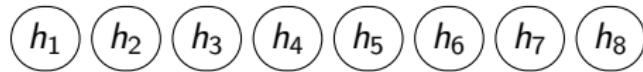
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Merkle trees

- ▶ One public key, multiple signatures?
 - ▶ OTS, so multiple signatures → multiple private keys

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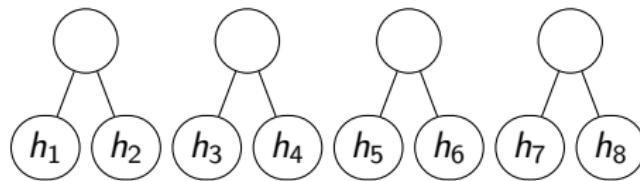
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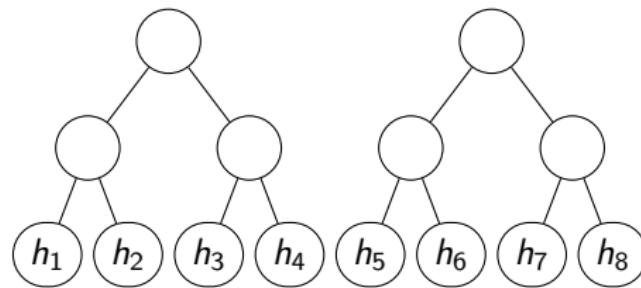
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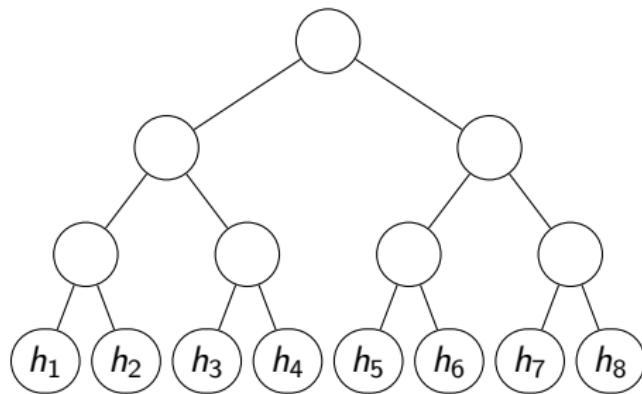
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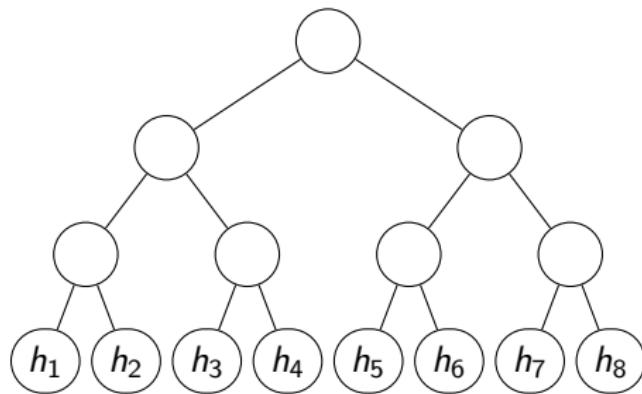
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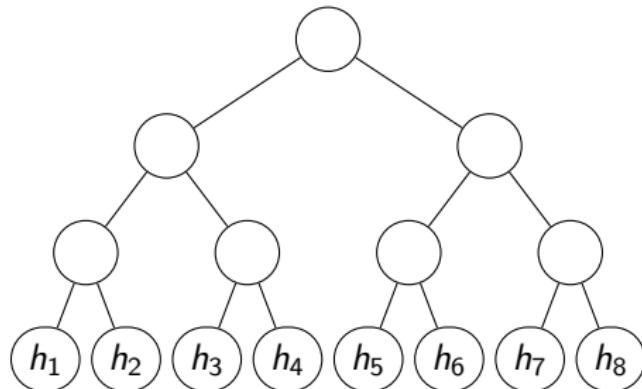
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- ▶ New public key: root node

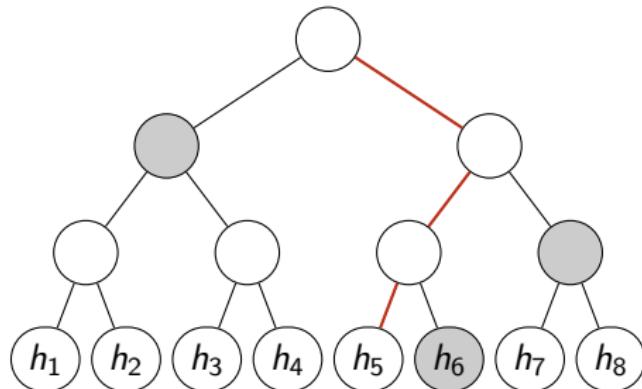
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- ▶ Signature must now include:
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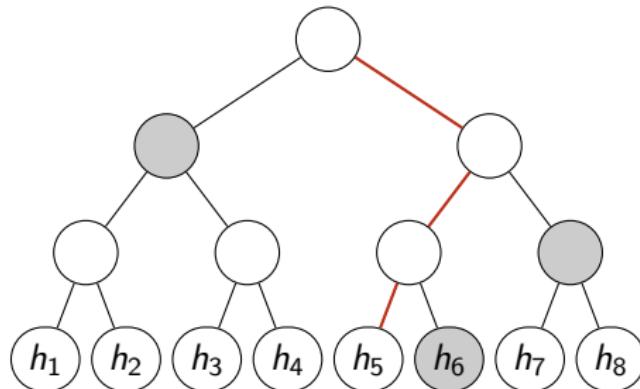
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- ▶ Verification: reconstruct root node

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- ▶ Signing is fast
- ▶ Keys are small
 - ▶ Private key generated from small seed
- ▶ Signatures are somewhat large..

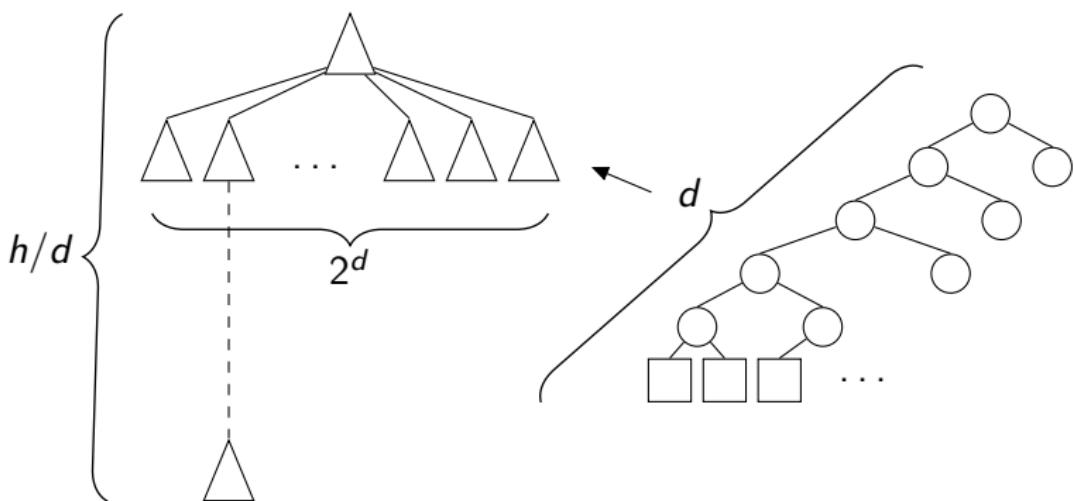
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- ▶ Need to **remember** the last used index!
 - ▶ Terribly inconvenient

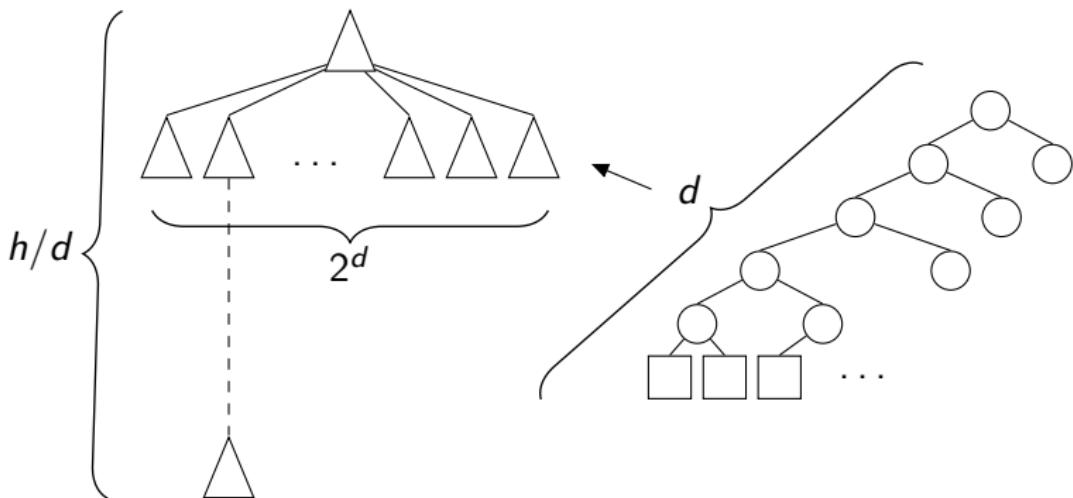
SPHINCS

- ▶ Large Merkle tree, height h
- ▶ Every d -th layer signs child node using an OTS
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- ▶ Layers of OTS: no need to compute entire tree
- ▶ Layers of hashing: acceptable signature size
- ▶ ‘Few time signature scheme’ (FTS) for leaf nodes
- ▶ Chance of a break becomes negligible

Key generation

- ▶ Generate random values SK_1 and SK_2
- ▶ Use SK_1 : generate OTS keys of top sub-tree
- ▶ Compute root node (recall: the sub-tree is a Merkle tree)
 - ▶ PK: root node

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- ▶ In general: SK_1 generates OTS and FTS keys *deterministically*

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- ▶ Repeat..

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- ▶ Repeat.. until root node
- ▶ Signature: $\Sigma = (R, \sigma_{FTS}, (\sigma_{OTS_1}, Auth_1), (\sigma_{OTS_2}, Auth_2), \dots, (\sigma_{OTS_{h/d}}, Auth_{h/d}))$

SPHINCS-256

- ▶ 41KB signatures, 1KB keys
- ▶ 256-bit hash functions
 - ▶ 128-bit post-quantum security
- ▶ $h = 60, d = 5$: 12 layers of sub-trees
- ▶ 2^{60} leaf nodes

Building blocks

- ▶ OTS
- ▶ Hash functions
- ▶ Key expansion function
- ▶ FTS

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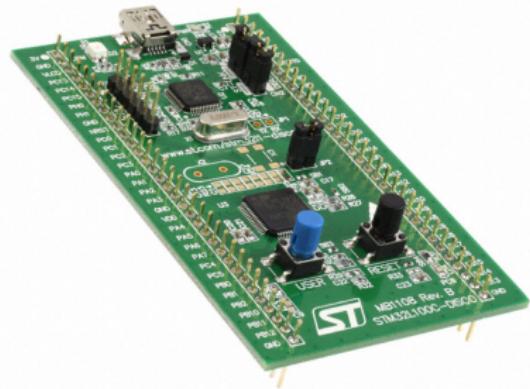
- ▶ OTS: *Winternitz OTS variant (WOTS+)*
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- ▶ FTS: *HORST*
 - ▶ Contains 16-layer Merkle tree (so 2^{16} leafs)
 - ▶ Goal: 32 authentication paths, root node
 - ▶ Complete tree takes approx. 2MB RAM..

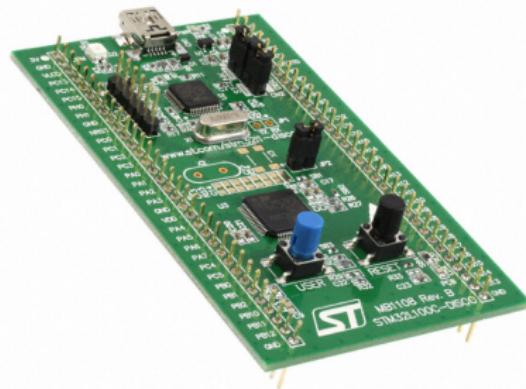
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- ▶ STM32L100C board with Cortex M3
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- ▶ Based on SPHINCS-256 for Haswell
 - ▶ Replaced `asm` with other implementations

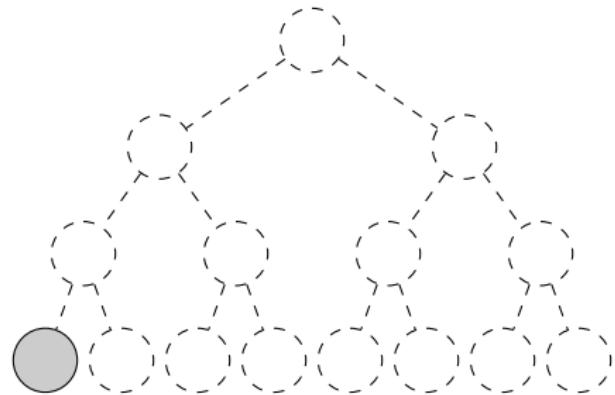


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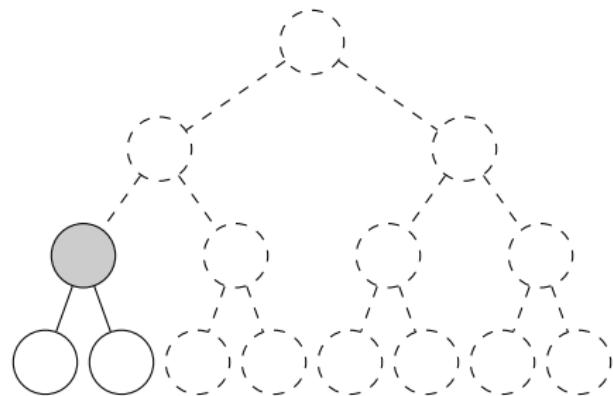
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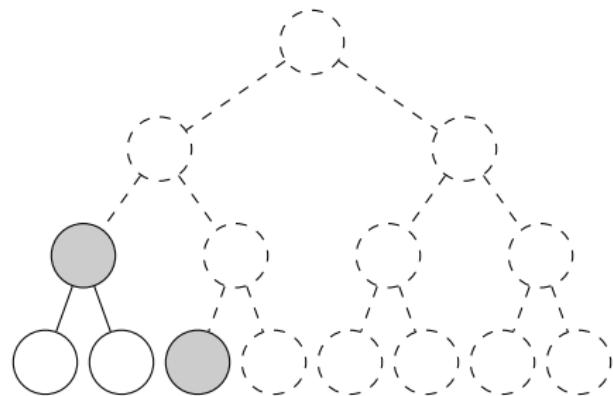
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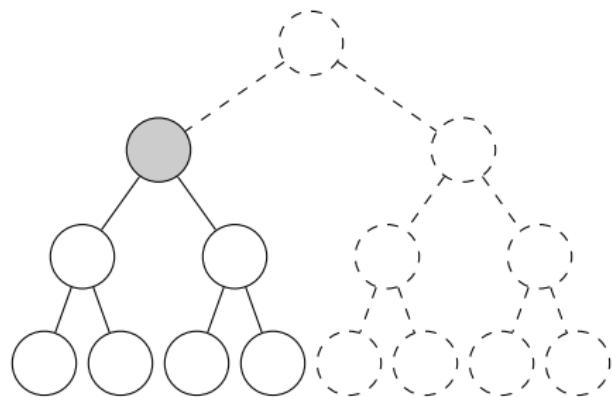
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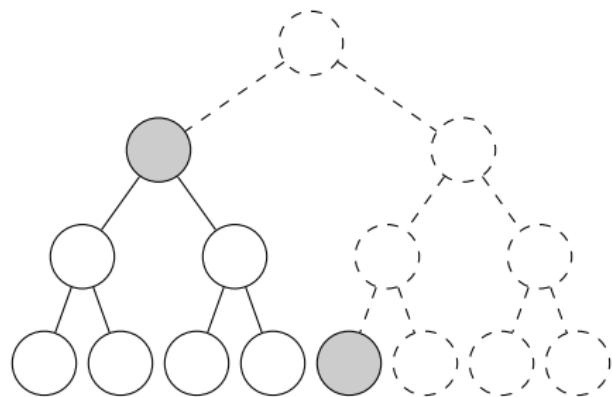
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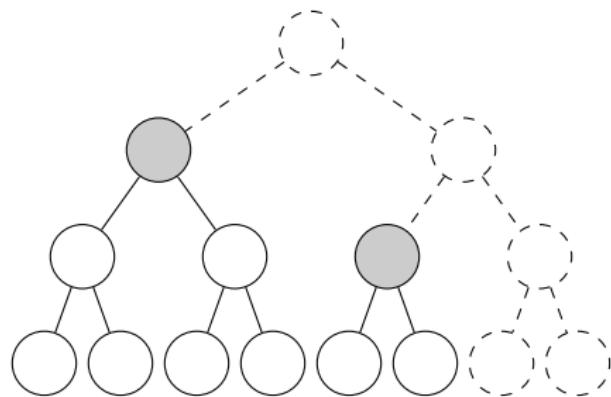
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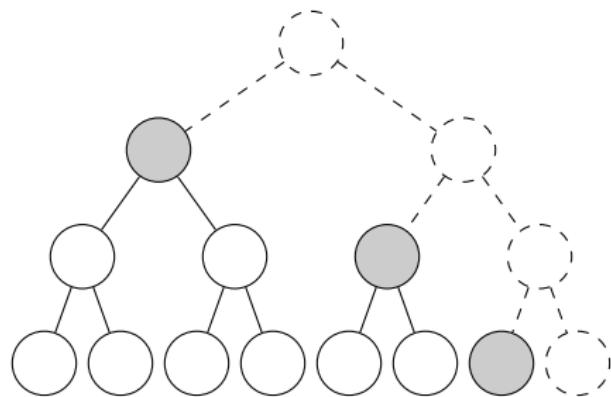
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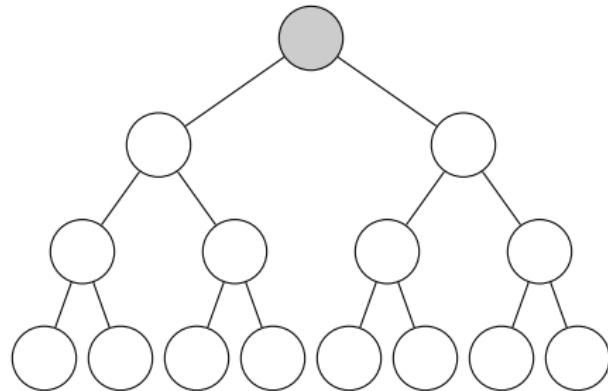
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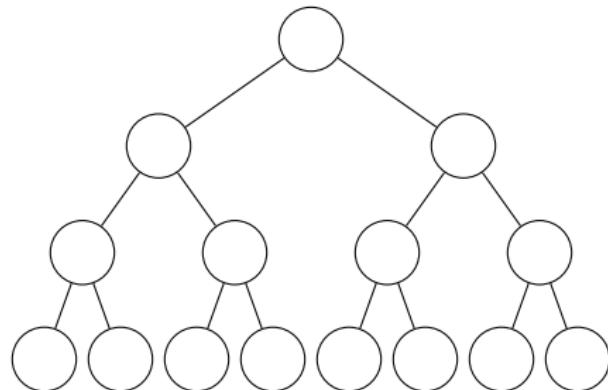
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- ▶ Output in the appropriate order..

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- ▶ Cannot store expanded key material
- ▶ Interleave ChaCha12 and Treehash

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“[..] signs hundreds of messages per second.”
- ▶ Hash-based? ChaCha cycles account for nearly 70%!

TODO

- ▶ Implement verification
- ▶ Implement ChaCha in ARMv7-M asm
- ▶ Operate on messages of arbitrary size
- ▶ Cache (partial) authentication paths

Conclusions

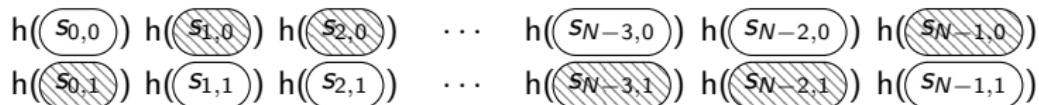
- ▶ SPHINCS could replace RSA / ECC / ... for signing
 - ▶ Stateless → drop-in replacement
 - ▶ Conservative security choice
- ▶ Feasible on limited platforms
 - ▶ Hard memory limit: ✓
 - ▶ Time efficiency: gradual optimisation

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- ▶ Verification: complete hashes to w , check with public key

HORST

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- ▶ Private key: t random numbers s_0, s_1, \dots, s_{t-1}
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 - ▶ Reveal $s_{m_0}, s_{m_1}, \dots, s_{m_{k-1}}$
 - ▶ Include authentication paths
- ▶ Very small chance of re-use